

# MBR40H100WTG

## Switch Mode Power Rectifier 100 V, 40 A

### Features and Benefits

- Low Forward Voltage
- Low Power Loss/High Efficiency
- High Surge Capacity
- 175°C Operating Junction Temperature
- 40 A Total (20 A Per Diode Leg)
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### Applications

- Power Supply – Output Rectification
- Power Management
- Instrumentation

### Mechanical Characteristics:

- Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 4.3 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes:  
260°C Max. for 10 Seconds

### MAXIMUM RATINGS

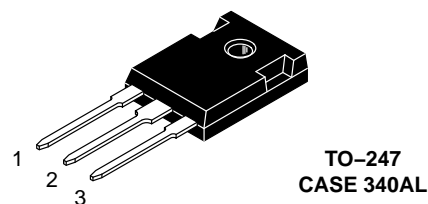
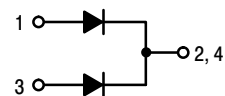
Please See the Table on the Following Page



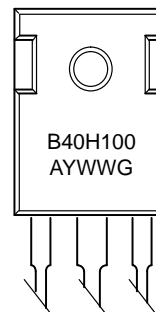
**ON Semiconductor®**

<http://onsemi.com>

**SCHOTTKY BARRIER  
RECTIFIER  
40 AMPERES  
100 VOLTS**



### MARKING DIAGRAM



B40H100 = Specific Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
G = Pb-Free Package

### ORDERING INFORMATION

Device	Package	Shipping
MBR40H100WTG	TO-247 (Pb-Free)	30 Units/Rail

# MBR40H100WTG

## MAXIMUM RATINGS (Per Diode Leg)

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	$V_{RRM}$ $V_{RWM}$ $V_R$	100	V
Average Rectified Forward Current $T_C = 148^\circ\text{C}$ , per Diode per Device	$I_{F(AV)}$	20 40	A
Peak Repetitive Forward Current (Square Wave, 20 kHz) $T_C = 144^\circ\text{C}$	$I_{FRM}$	40	A
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	$I_{FSM}$	200	A
Operating Junction Temperature (Note 1)	$T_J$	+175	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-65 to +175	$^\circ\text{C}$
Voltage Rate of Change (Rated $V_R$ )	dv/dt	10,000	V/ $\mu\text{s}$
Controlled Avalanche Energy (see test conditions in Figures 10 and 11)	$W_{AVAIL}$	400	mJ
ESD Ratings: Machine Model = C Human Body Model = 3B		> 400 > 8000	V

## THERMAL CHARACTERISTICS

Maximum Thermal Resistance – Junction-to-Case – Junction-to-Ambient (Socket Mounted)	$R_{\theta JC}$ $R_{\theta JA}$	0.58 32	$^\circ\text{C/W}$
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Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## ELECTRICAL CHARACTERISTICS

Characterisitic	Symbol	Min	Typ	Max	Unit
Instantaneous Forward Voltage (Note 2) ( $I_F = 20\text{ A}$ , $T_J = 25^\circ\text{C}$ ) ( $I_F = 20\text{ A}$ , $T_J = 125^\circ\text{C}$ ) ( $I_F = 40\text{ A}$ , $T_J = 25^\circ\text{C}$ ) ( $I_F = 40\text{ A}$ , $T_J = 125^\circ\text{C}$ )	$v_F$	-	0.74 0.61 0.85 0.72	0.80 0.67 0.90 0.76	V
Instantaneous Reverse Current (Note 2) (Rated dc Voltage, $T_J = 125^\circ\text{C}$ ) (Rated dc Voltage, $T_J = 25^\circ\text{C}$ )	$i_R$	-	2.0 0.0012	10 0.01	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- The heat generated must be less than the thermal conductivity from Junction-to-Ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ .
- Pulse Test: Pulse Width = 300  $\mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

# MBR40H100WTG

## TYPICAL CHARACTERISTICS

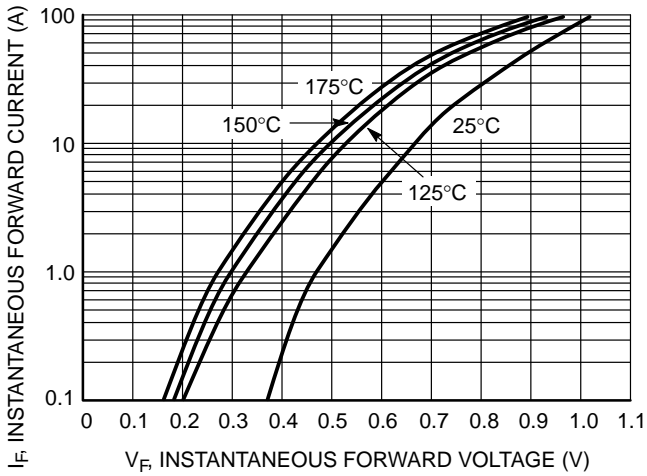


Figure 1. Typical Forward Voltage

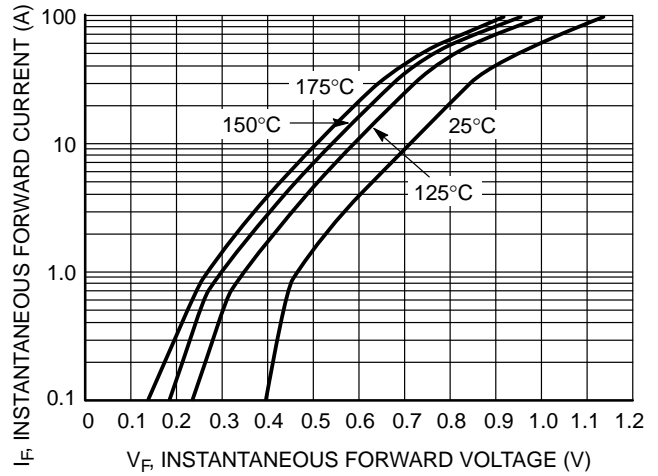


Figure 2. Maximum Forward Voltage

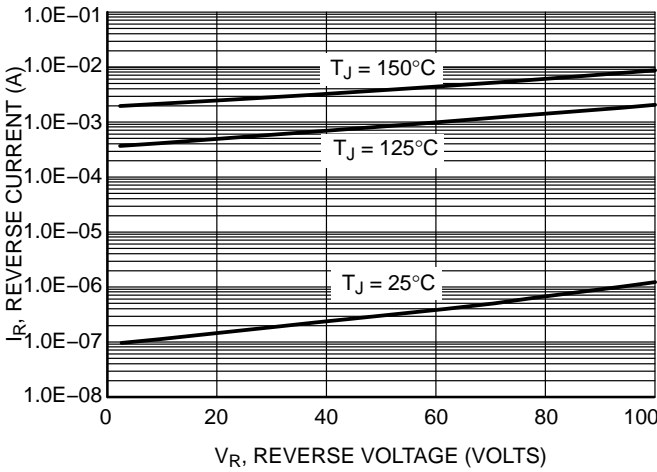


Figure 3. Typical Reverse Current

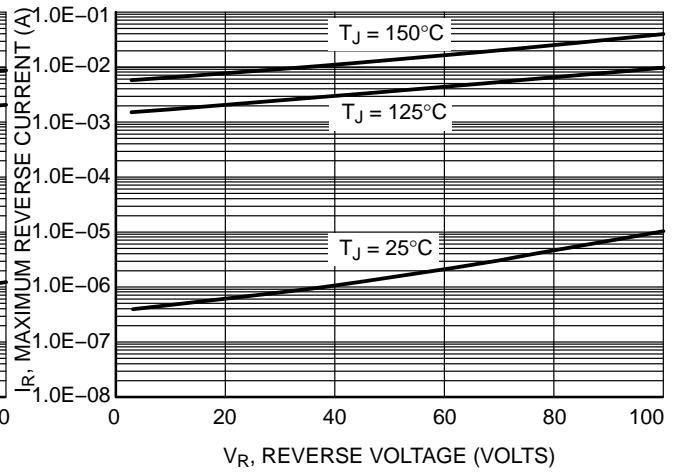


Figure 4. Maximum Reverse Current

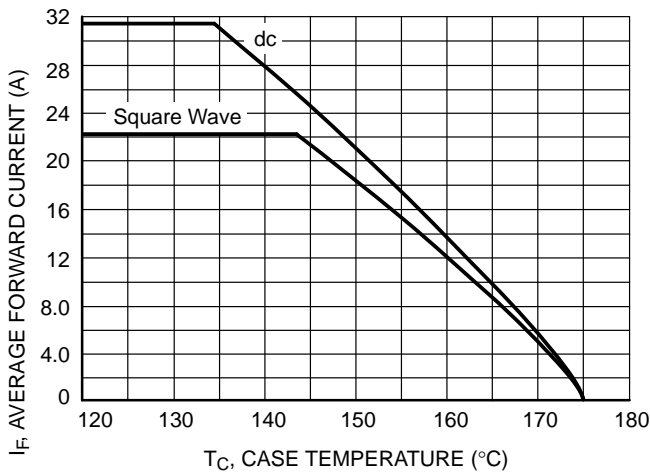


Figure 5. Current Derating, Case, Per Leg

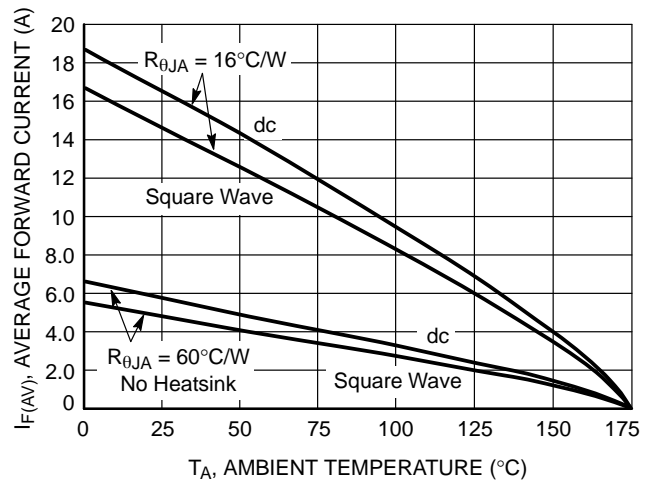


Figure 6. Current Derating, Ambient, Per Leg

# MBR40H100WTG

## TYPICAL CHARACTERISTICS

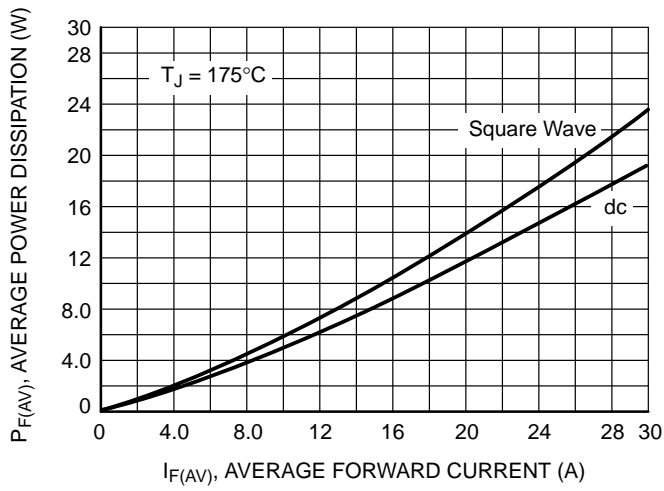


Figure 7. Forward Power Dissipation

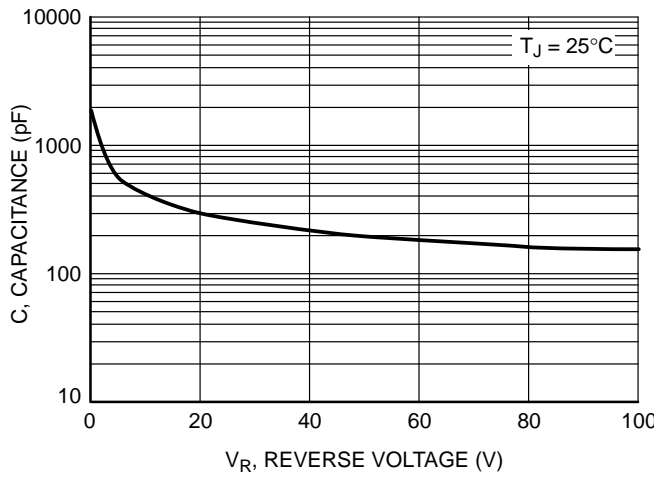


Figure 8. Capacitance

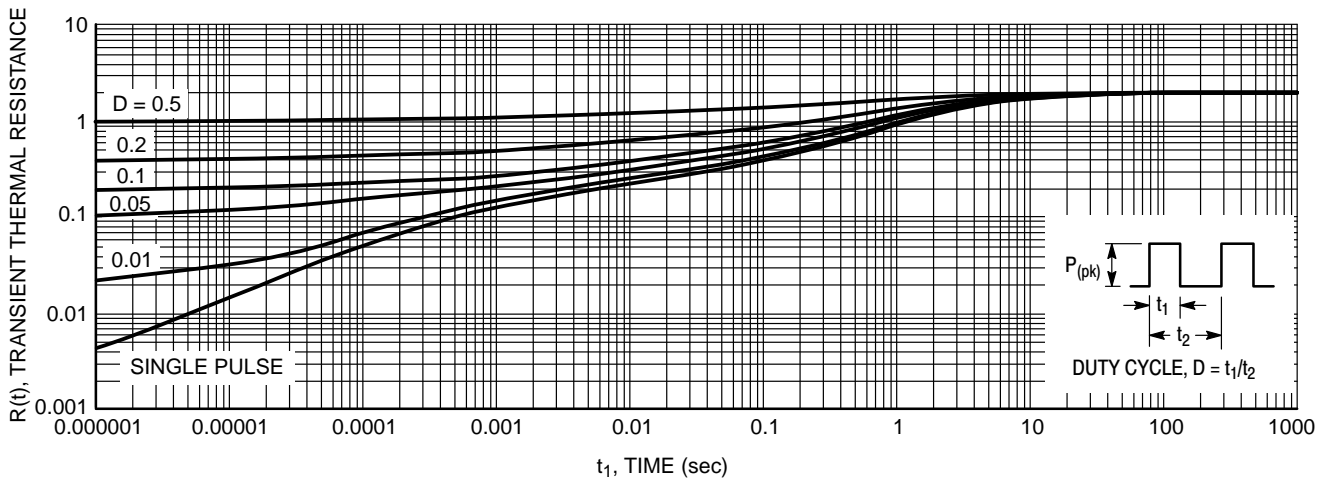


Figure 9. Thermal Response Junction-to-Case

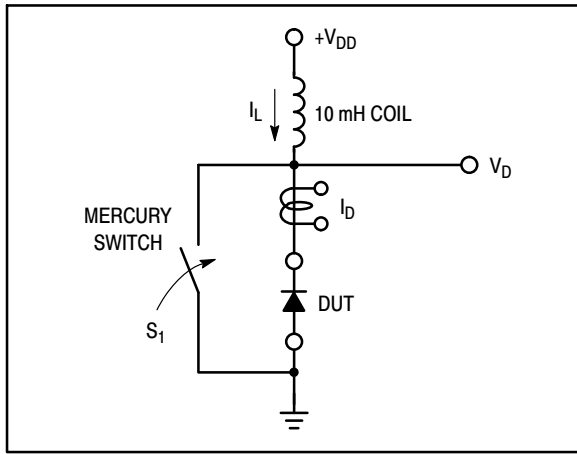


Figure 10. Test Circuit

The unclamped inductive switching circuit shown in Figure 10 was used to demonstrate the controlled avalanche capability of this device. A mercury switch was used instead of an electronic switch to simulate a noisy environment when the switch was being opened.

When  $S_1$  is closed at  $t_0$  the current in the inductor  $I_L$  ramps up linearly; and energy is stored in the coil. At  $t_1$  the switch is opened and the voltage across the diode under test begins to rise rapidly, due to  $di/dt$  effects, when this induced voltage reaches the breakdown voltage of the diode, it is clamped at  $BV_{DUT}$  and the diode begins to conduct the full load current which now starts to decay linearly through the diode, and goes to zero at  $t_2$ .

By solving the loop equation at the point in time when  $S_1$  is opened; and calculating the energy that is transferred to the diode it can be shown that the total energy transferred is equal to the energy stored in the inductor plus a finite amount of energy from the  $V_{DD}$  power supply while the diode is in breakdown (from  $t_1$  to  $t_2$ ) minus any losses due to finite component resistances. Assuming the component resistive

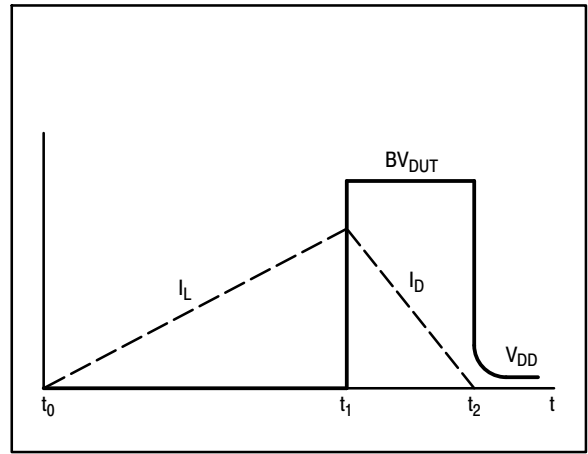


Figure 11. Current-Voltage Waveforms

elements are small Equation (1) approximates the total energy transferred to the diode. It can be seen from this equation that if the  $V_{DD}$  voltage is low compared to the breakdown voltage of the device, the amount of energy contributed by the supply during breakdown is small and the total energy can be assumed to be nearly equal to the energy stored in the coil during the time when  $S_1$  was closed, Equation (2).

EQUATION (1):

$$W_{AVAL} \approx \frac{1}{2} L I_{LPK}^2 \left( \frac{BV_{DUT}}{BV_{DUT} - V_{DD}} \right)$$

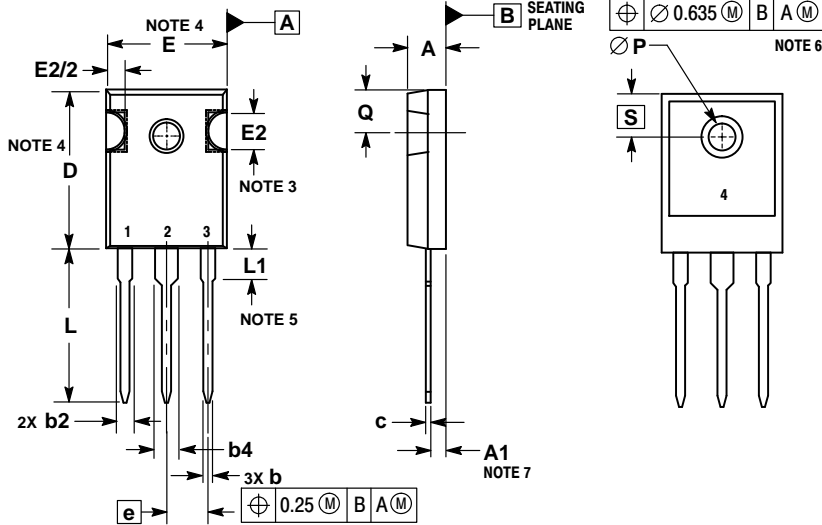
EQUATION (2):

$$W_{AVAL} \approx \frac{1}{2} L I_{LPK}^2$$

# MBR40H100WTG

## PACKAGE DIMENSIONS


### TO-247 CASE 340AL ISSUE A



#### NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. SLOT REQUIRED, NOTCH MAY BE ROUNDED.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
5. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.
6.  $\varnothing P$  SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91.
7. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.

DIM	MILLIMETERS	
	MIN	MAX
A	4.70	5.30
A1	2.20	2.60
b	1.00	1.40
b2	1.65	2.35
b4	2.60	3.40
c	0.40	0.80
D	20.30	21.40
E	15.50	16.25
E2	4.32	5.49
e	5.45 BSC	
L	19.80	20.80
L1	3.50	4.50
P	3.55	3.65
Q	5.40	6.20
S	6.15 BSC	

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